

Veriest

Verification Internship



Veriest is an international ASIC design house that provides a range of engineering services. We are starting training for the next generation of HW engineers soon. If you want a profession in the semiconductor industry, the center of technology and development, make sure to sign up for this training below.

We offer a 3 months training program in:

- Introduction to VLSI
- System Verilog
- Universal Verification Methodologies (UVM)

Course Info

Total of 3 months training of frontal classes and online training & labs.

Qualifications

- Education: Electrical engineering graduate (B.SC) / Students in their final year
- Self-learning abilities
- Commitment and availability for the entire course duration

If you are looking for an environment where you can grow professionally, learn from the best in the field, balance work and life habits and have a pleasant stable environment, please submit your CV in English at jobs.serbia@veriests.com