

WHERE PASSION LEADS TO EXCELLENCE



ELSYS Eastern Europe, having its business in Belgrade and Novi Sad, Serbia, is a subsidiary of ADVANS Group. ADVANS Group is an international group of experts in software, electronic systems and mechanics. Our 1000 engineers serve a range of industries in: multimedia/wireless, medical, network/telecom, energy, aerospace, defense, automotive and IT.

For our office in **Belgrade**, Serbia, we are offering:

Silicon Validation/Characterization Engineer

1 positions - Internship and permanent job position

Internship description:

You will be part of the team working on **ADC integrated circuit** in cutting edge technologies developed at School of Electrical Engineering. In order to achieve IC implementation milestones you will be involved in validation methodology establishment, validation plan creation, development of validation environment and close interaction with other IC implementation team. Your work on these activities will be challenging and you will have the opportunity to work with industry experts on the state-of-the-art processes, tools and flows.

- Selected candidates will be engaged in development of automated validation environment including:
 - Testboard development - PCB Design
 - Develop and execute tests on the lab bench according to validation plan during post-silicon phases of the product to ensure functionality and performance as per the datasheet
 - Develop automated test framework and regression test suites in LabVIEW/TestStand to optimize validation efficiency
 - Collaborate closely with IC Design engineers, Systems engineers and Application engineers to understand requirements and use cases.
 - Debug analog/digital silicon issues, board issues and Validation software/hardware tools issues
 - Analyze test results and identify failures/defects
- Intern will receive full training and support from our experienced engineers.
- Internship duration is 3 to 6 months, with two starting dates:
 - **Round 1 - 3rd December**
 - **Round 2 - 18th February**
 - or differently defined with candidate
- Full time internship 40h a week or differently defined with candidate
- Internship project can be subject of a bachelor/master thesis or seminar paper
- Interns will receive monthly compensation and company will provide accommodation if they are outside of Belgrade

Required Skills and Qualifications:

- Final year student or fresh graduate with B.Sc. or M.Sc. degree in electrical engineering
- Motivated, proactive, hard working
- Relevant courses/knowledge: basics of digital electronics, programming, FPGA basics, microcontroller architecture
- VHDL or Verilog, C programming language, object-oriented programming is a plus
- Good knowledge of English language **is a plus**
- Experience with scripting or LabVIEW **is a plus**
- Experience in designing mixed signal boards **is a plus**
- Experience in lab equipment **is a plus**

Employee Benefits:

- Integration Program in a Professional, Young & Dynamic Team with Professional Development Opportunities
- International Work Environment & Traveling Opportunities

All interested candidates are invited to apply on following link www.elsys-eastern.com/career/internships with **CV and motivation letter in English**, indicating the field of interest, relevant college courses and projects. Deadline for application: **Round 1 - 17th November & Round 2 - 22nd December**. Interviews are planned from end of October; the final list of selected candidates will be announced by **Round 1 - 22nd November & Round 2 - 15th January**.